

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1	((software controller) and mechanism and switch and termination and (enablement adj signal)).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/23 17:20
L2	28782	enable and controller and switch and resistor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/23 17:20
L3	427	2 and "326"/\$7.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/23 17:21
L4	0	3 and (softwre adj controller)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/08/23 17:21
S1	387	terminat\$3 and (control adj signal) and (switch or transistor) and 326/30.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/22 20:21
S2	365	S1 and (resistor or resistance)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/22 20:15
S3	43	S2 and (SCSI or PCI)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/22 20:15
S4	786	terminat\$3 and (switch or transistor) and 326/30.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/22 20:15

S5	739	S4 and (resistor or resistance)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/22 20:15
S6	78	S5 and (SCSI or PCI)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/22 20:22
S7	35	S6 not S3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/22 20:16
S8	67774	terminat\$3 and (control adj signal) and (switch or transistor)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/22 20:22
S10	3297	S8 and (SCSI or PCI)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/22 20:22
S11	890	S10 and resistor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/22 20:45
S12	29	S11 and (software adj controller)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/22 20:23
S13	123	S10 and "326"/\$7.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/22 20:35

S14	50	S10 and "326"/30.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/22 20:36
S15	126	(scsi or pci) and "326"/30.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/22 20:36
S16	76	S15 not S14	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/22 20:37
S17	492	S11 and transistor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/22 20:45
S18	119	S17 and (PLD or CPLD or FPGA)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/06/22 20:46
S19	1523	(326/30).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/23 15:51
S20	799	(326/21).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/23 15:58
S21	0	("002not1").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/23 15:52
S22	587	S20 not S19	USPAT	OR	OFF	2005/08/23 15:52

S23	536	(326/82).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/23 16:04
S24	0	("5not(1and2)").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/23 15:58
S25	447	S23 not (S19 and S20)	USPAT	OR	OFF	2005/08/23 16:05
S26	1705	(326/83).CCLS.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/23 16:05
S27	447	S25 not (S19 and S20 and S23)	USPAT	OR	OFF	2005/08/23 16:05